

FROM HIGH LEVEL SPECIFICATIONS TO REQUIREMENTS: SECURING NEXT-GEN SoC SUCCESS



THE CLIENT

A blue-chip company in smart energy systems entering a critical phase: the development of a custom SoC to boost performance and differentiation in their next-generation product.

THE CHALLENGE

Our client appointed a major chip maker to scale up from a previous generation AMS ASIC to a **specialized SoC embedding smart processing**. Our client required help to formalize their needs and acquire key digital project management capabilities.

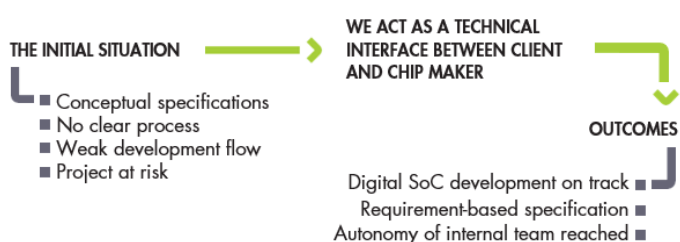
Technical support needs:

- Building internal digital expertise, as no SoC has been developed in-house before
- Formalizing new requirements from previous project specifications and current needs.
- Establishing a proper digital verification flow from specification to testbench

Project management needs:

- Structuring communication processes to improve project visibility
- Clarifying expectations between the client and the chip maker

THE PROCESS PATH



OUR SOLUTION

Initially brought in to update the specification and enable test plan generation, our role quickly evolved into a **technical bridge between the client and the chip maker**, translating needs, aligning expectations, and ensuring visibility.

We helped to:

- Formalize the SoC specification,
- Design a requirement-based verification plan,
- Develop a dedicated test environment,
- Setup a Continuous-Integration workflow.

RESULTS ACHIEVED

The client gained **clarity, technical oversight, and peace of mind**.

- **A solid and traceable** digital specification
- **Restored internal ownership** of digital aspects through tutoring and methodology sharing
- **Confidence restored** in the ASIC development process and planning

